



10/776,215

CFC
cc

520.40885VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: J. YUGAMI, et al.
Patent No.: 6,833,296
Issued: December 21, 2004
Application No.: 10/776,215
Filed: February 12, 2004
For: METHOD OF MAKING A MISFET SEMICONDUCTOR
DEVICE HAVING A HIGH DIELECTRIC CONSTANT
INSULATING FILM WITH TAPERED END PORTIONS
Group Art Unit: 2812
Examiner: Scott B. Geyer

**REQUEST FOR CERTIFICATE OF CORRECTION
UNDER 37 C.F.R. §1.323 AND 35 U.S.C. §255**

**Certificate
MAR 27 2007
of Correction**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 22, 2007
03/23/2007 YPOLITE1 00000111 6833296
01 FC:1811

100.00 OP

Sir:

In the matter of the above-identified U.S. Letters Patent, it is respectfully requested that a Certificate of Correction be issued, in accordance with the provisions of 37 C.F.R. §1.323 and pursuant to 35 U.S.C. §255, correcting the errors as shown in attached Form PTO/SB/44. An authorized Credit Card payment form covering the fee amount set forth in 37 C.F.R. §1.20(a), for consideration and issuance of a Certificate of Correction under this Section, is enclosed herewith.

The discovered printing error in question concerns an incorrect filing date pertaining to the priority Japanese Patent Application No. 2000-375610, which was filed in Japan on December 11, 2000. The prefix in the priority Japanese Application is "2000", which means that it is an application that was filed in Japan in the year 2000, which agrees with its official Japanese filing date of December 11, 2000.

MAR 27 2007

However, a review of the file history shows that this Japanese filing date of December 11, 2000 was incorrectly posted in the Declaration as December 11, 2001. This error, it is submitted is clearly a typographical error noting that a separate paper was also filed in the present application entitled "Claim for Priority", as well as in the parent application of the corresponding series of continuing applications, in which Applicants claim the benefit under 35 U.S.C. §119 (37 C.F.R. §1.55) based on the counterpart Japanese Application No. 2000-375610, filed in Japan on December 11, 2000. As evidence of this, attached hereto is a copy of the paper in question (Exhibit A) which was downloaded from the U.S. PTO Private PAIR website. Also attached hereto, as Exhibit B, is a copy of the first page of the Certified Copy of the priority Japanese Application and a copy of the cover letter thereof which were downloaded from the U.S. PTO Private PAIR website, with regard to the official file history of the parent application (now U.S. Patent 6,710,383). As can be seen from Exhibit B, the priority Japanese Application No. 2000-375610 was filed (in Japan) on December 11, 2000. It is evident therefore that the above discussed error in question was a mistake of a minor character, namely, an error of a typographical nature, and, therefore, meets the criteria for issuance of a Certificate of Correction Pursuant to 37 C.F.R. §1.323 (35 U.S.C. §255).

Therefore, since this request for issuance of a Certificate of correction concerns a correction of an obvious minor formal nature, in accordance with that permitted under 37 C.F.R. §1.322 and 35 U.S.C. §255, as well as the guidelines permitted in M.P.E.P. §1481, the issuance of a Certificate of Correction in line with that noted in attached Form PTO/SB/44 is respectfully requested.

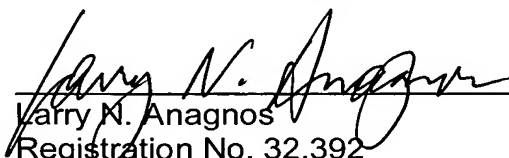
MAR 27 2001

U.S. Patent 7,064,400
520.40855CV2

Please charge any shortage in fees due in connection with the filing of this paper to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (520.40885VX1) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



Larry N. Anagnos
Registration No. 32,392
(703) 312-6600

Enclosures

LNA:dlh

MAR 27 2007

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO: 6,833,296
APPLICATION NO.: 10/776,215
ISSUE DATE: FEBRUARY 21, 2004
INVENTOR(S): J. YUGAMI et al.

It is certified that an error appears or errors appear in the above-identified patent
and that said Letters Patent is hereby corrected as shown below:

"(30) Foreign Application Priority Data

Dec. 11, 2000 (JP).....2000-375610"

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Antonelli, Terry, Stout & Kraus, LLP, 1300 North Seventeenth Street, Suite 1800, Arlington, VA 22209

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

MAR 27 2007

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

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APPLICATION NO.: 10/776,215
ISSUE DATE: FEBRUARY 21, 2004
INVENTOR(S): J. YUGAMI et al.

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MAR 27 2007



EXHIBIT A

520.40885VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: J. YUGAMI, et al.
Application No.: Rule 1.53(b) Divisional of U.S. Patent Application Serial No. 10/005,355, filed December 7, 2001
Filed: On Even Date Herewith
For: SEMICONDUCTOR DEVICE AND PROCESS FOR PRODUCING THE SAME
Art Group of Parent: 2829
Examiner of Parent: S. B. Geyer

CLAIM FOR PRIORITY

Commissioner for Patents
P. O. BOX 1450
Alexandria, VA 22313-1450

February 12, 2004

Sir:

Pursuant to 35 U.S.C. § 119 and 37 C.F.R. § 1.55, Applicants hereby claim the right of priority based on the following foreign application filed in Japan:


Application No. 2000-375610, filed December 11, 2000.

A certified copy of this foreign application was filed on December 7, 2001 in U.S. Application Serial No. 10/005,355.

Kindly charge any additional fees due, or credit overpayment of fees, to Deposit Account No. 01-2135 (520.40885X00).

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP

By



Gregory E. Montone
Reg. No. 28,141

GEM/dlt

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Arlington, Virginia 22209
Telephone: (703) 312-6600
Facsimile: (703) 312-6666

MAR 27 2007

日 本 国 特 許 庁
JAPAN PATENT OFFICE

jc997 U.S. PTO
10/005355
12/07/01

別紙添付の書類に記載されている事項は下記の出願書類に記載されている事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed with this Office

出 願 年 月 日
Date of Application:

2000年12月11日

出 願 番 号
Application Number:

特願2000-375610

出 願 人
Applicant(s):

株式会社日立製作所

BEST AVAILABLE COPY

2001年11月 2日

特 許 庁 長 官
Commissioner,
Japan Patent Office

及 川 耕 造

MAR 27 2007

出証番号 出証特2001-3096927

MAR 27 2007

REST AVAILABLE COPY

10/005355
12/07/01
10/005355

520.40885X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): YUGAMI, et al.
Serial No.: Not assigned
Filed: December 7, 2001
Title: SEMICONDUCTOR DEVICE AND PROCESS FOR
PRODUCING THE SAME
Group: Not assigned

3. Priority Paper
1. Step 10L
3-800

LETTER CLAIMING RIGHT OF PRIORITY

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

December 7, 2001

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55, the
applicant(s) hereby claim(s) the right of priority based on
Japanese Application No. (s) 2000-375610 filed December 11, 2000.

A certified copy of said Japanese Application is attached.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Gregory E. Montone
Gregory E. Montone *Ronald J. Shaw*
Registration No. 20,141 28, 77

GEM/amr
Attachment
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MAR 27 2007